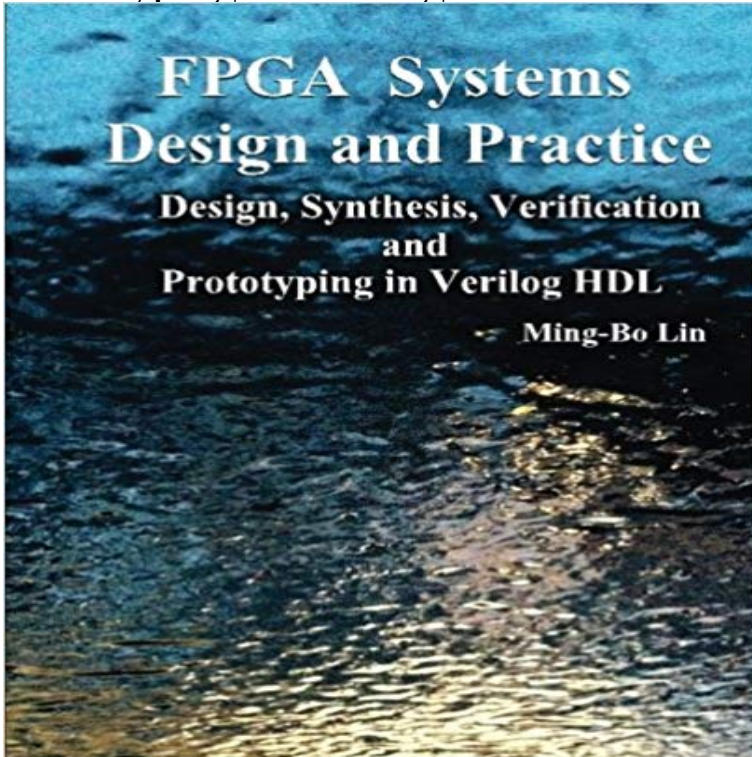


# FPGA Systems Design and Practice: Design, Synthesis, Verification, and Prototyping in Verilog HDL



With the advance of semiconductor and communication industry, the use of system-on-chip (SoC) has become an essential technique to reduce product costs. The development of a good understanding of the key stages of the hardware description language (HDL) design flow based on cell-based libraries or field-programmable gate array (FPGA) devices becomes essential. This book addresses the needs for such a topic based on Verilog HDL and FPGAs. The most important features of this book include

- Abundant examples: A lot of worked examples and abundant review questions are provided in each section for readers to test their understanding of the related topics.
- Abundant end-of-chapter problems: A lot of end-of-chapter problems are included in each chapter to provide readers more opportunities to practice what they learn from the chapter.
- Plentiful case studies: Plentiful case studies are included to help the reader grasp the essentials of practical digital systems with Verilog HDL and FPGAs.
- Highlighting Verilog HDL syntax: The Verilog HDL syntax is highlighted to aim the reader to refer the desired syntax as needed.
- Bolded keywords in Verilog HDL modules: All keywords in Verilog HDL modules throughout the book are bolded to emphasize the language structures and improve the readability of the modules

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## LANGUAGES

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